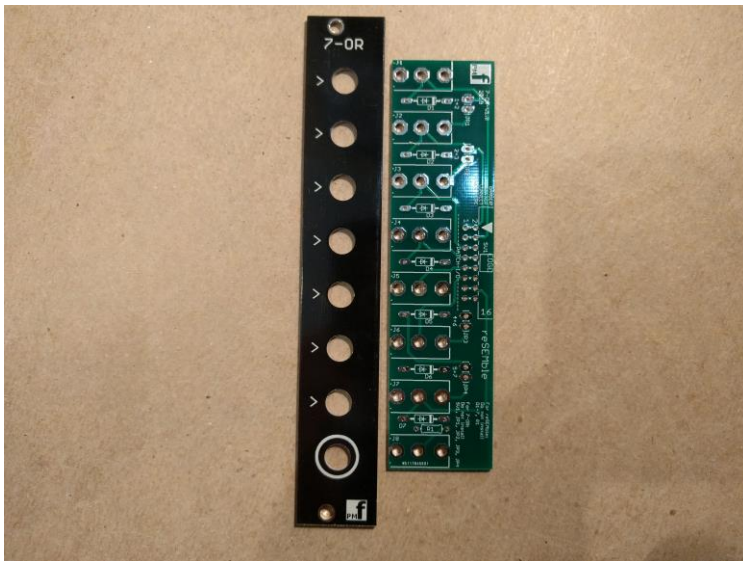


7-OR v1.0 – Assembly Guide

Thank you for purchasing this module! This is an easy build. Some of the pads are quite small and you will need a chisel tip or screwdriver tip soldering iron and the skill to solder these tiny joints.

This module is also used in the reSEMble semi-modular synth. **YOU DO NOT NEED TO AND SHOULD NOT INSTALL THE PARTS that are specific to the reSEMble synth. These parts are not included in the BOM. These are the parts marked on the PCB that are not required:**

SV1, JP1, JP2, JP3, JP4



The module is designed and sized for **Euro rack** systems. You will need a 16-10 pin eurorack power ribbon connector with $-12/0/+12$ which is connected to a synth power supply.

Follow the parts lists, these instructions and the PCB silkscreen text to build the module.

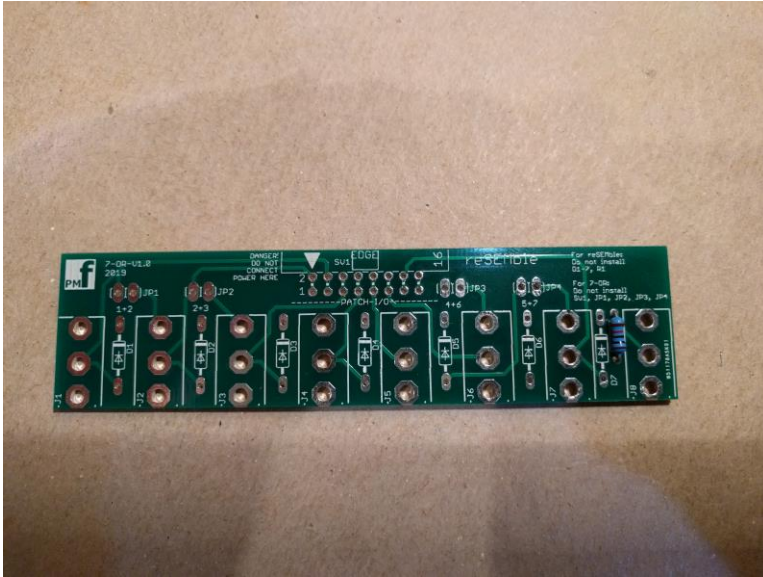
The module consists of 1 PCB board and a front panel.

You must follow the order of assembly as described below since some components will be soldered underneath other components.

Constructing the board

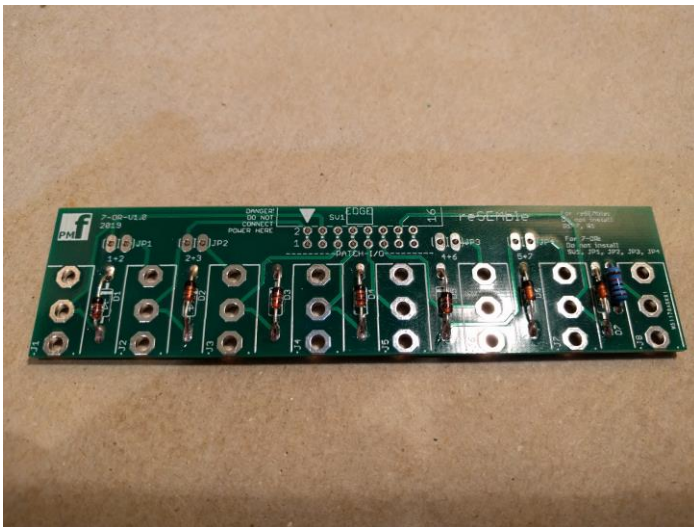
1. Resistor

Install the flat resistor on the TOP of the board. Solder and clip the leads.



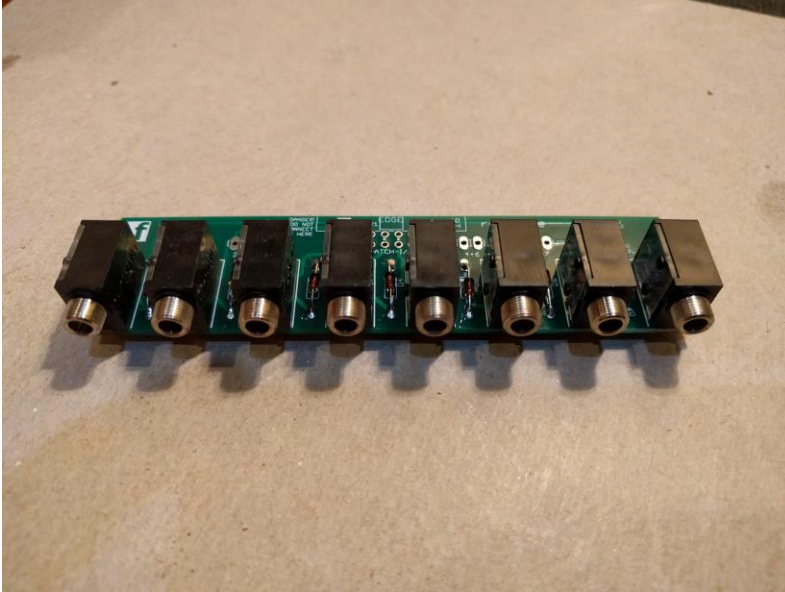
2. Diodes

Install the diodes on the TOP of the board. Align the stripe on each diode with the stripe marked on the board. Solder and clip the leads. Now go back and check the polarity against the silk screen for each diode.



3. 3.5mm Jack Sockets

Tack one pin of each only with solder. These will be finalized later. Please ensure they are on the CORRECT SIDE OF THE BOARD.



4. Alignment

1. Place the front panel over the board so that the 3.5mm jacks and pot align with the holes in the panel. The jacks will be on the right when the panel is the correct way up. The resistor will be towards the bottom near the hole marked with a white circle.
2. Put nuts on the jacks and FULLY TIGHTEN all of them. Do not overtighten!
3. Now fully solder the remaining jack pins.
4. Cut the pins of the jack sockets below the panel edge to avoid contact with other modules in your rack.